

Controlling the Technovision LD48 through TTL

External Controls to the LD48 (TTL)

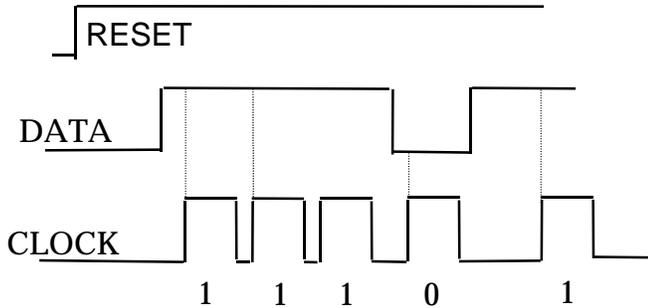
Connector _____ DB9M connector on the LD48

Signals:

RESET _____ Pin 1 (a low signal will reset all outputs to OFF)

DATA _____ Pin 4 (TTL level)

CLOCK _____ Pin 6 (TTL level)



Maximum CLOCK speed is 30kHz and must remain high for at least 6 μ s.

As the data is clocked into the board the bits are shifted each time - the last data bit clocked into the board is always the data bit for terminal 1. For example: if 10 bits are clocked in, the first bit is for terminal 10 and the last bit is for terminal 1. Data is clocked in on the rising edge of the clock line.

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